



WCEM, EXAMINATION CELL

Date: 12/09/2025

**Timetable for Master of Technology (MTech) First Semester
 End Semester Examination (ESE) Winter-2025 Backlog (Ex-Student) Examination**

Programme: Written
 Time: *11.00 AM To 02.00 PM

Sr. No	Name of Branch	Date Day	23-09-25 Tuesday	25-09-25 Thursday	27-09-25 Saturday	29-09-25 Monday	01-10-25 Wednesday	04-10-25 Saturday
1	Artificial Intelligence and Data Science (AIDS)		Artificial Intelligence and Expert Systems	Machine Learning	Advanced Algorithm	Research Methodology and Intellectual Property Rights	<u>Program Elective-I</u> 1. Big Data Analytics 2. Blockchain Application Development	<u>Program Elective-II</u> 1. Sensor Network & Internet of things 2. Digital Forensics
2	Computer Aided Structural Engineering (CASE)		Advanced Concrete Structure Time: *11.00 AM To 03.00 PM	Structural Dynamics	Foundation Design	Theory of Elasticity	<u>Program Elective-I</u> 1. Matrix Analysis of Structures 2. Earthquake Resistant Design of Steel Structures	<u>Program Elective-II</u> 1. Road Safety Engineering 2. Global Warming and Climate Change
3	Computer Science and Engineering (CSE)		Advanced Computing Architecture	Advances in Operating System Design	Data Science using Python	Digital Image Processing & Computer Vision	<u>Elective-I</u> Pattern Recognition 2. Optimization Techniques in Artificial Intelligence	<u>Elective-II</u> 1. Advance Data Mining and Big Data Analytics 2. Cyber Forensic and Computer Crimes



7391058481/82
contact@wcem.edu.in
www.wcem.edu.in

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1. The first step is to identify the problem or question that needs to be answered. This involves understanding the context and the specific requirements of the task.

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		Tuesday	Thursday	Saturday	Monday
4	CAD/CAM	Computer Integrated Manufacturing	Computer Graphics	CNC and Robotics	<u>Elective-I</u> 1. Design for Manufacturing & Assembly 2. Material Engineering 3. Mechanical behaviour of Engineering Materials
					<u>Elective-II</u> 1. Total Quality System & Engineering 2. Concurrent Engineering 3. Advanced Machining Process
5	Electronics Engineering (EE)	Advanced Digital Signal Processing	CMOS VLSI Design	Advanced Embedded System Design	<u>Program Elective-I</u> 1. Pattern Recognition 2. Analog IC Design 3. Advanced Digital Communication
					<u>Open Elective I</u> 1. Advance Data Mining and Big Data Analytics 2. Optimization Techniques in Artificial Intelligence 3. Cyber Forensic and Computer Crimes

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Controller of Examinations
4, Bharat

Engineering and Management
Dongarsan, Nagpur



Wainganga Bahu-Uddeshiya Vikas Sanstha's
WAINGANGA COLLEGE
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6	Power Electronics and Power System (PEPS)	Advanced Power Electronics	Advanced Power System Protection	FACTS & HVDC Transmission	Research Methodology	<u>Program Elective-I</u> 1. Microcontroller Application in PE & PS 2. Hybrid Electric Vehicle	<u>Group Elective</u> 1. Electrical Power Distribution & Smart Grid 2. AI Application to Power System
7	Software Systems	Advances in Algorithms	Advances in System Software Design	Software Architecture	Computer Organization and Software System	<u>Program Elective-I</u> 1. Secure Software Engineering 2. Cyber Security	<u>Program Elective-II</u> 1. Security in IOT 2. Soft Computing

Copy to:

1. The Director
2. All MTech HoD's
3. Examination Control Room
4. Admin Office

Issued by:

Dr. Sachin Y. Zade

Controller of Examinations

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Engineering and Management

Dongargaon, Nagpur